: count 10 0 do i 2 +loop ; count

step: 0

[entrypoint]

97	CALL 84
98	RET

Data:

Return:

: count 10 0 do i 2 +loop ; count

step: 1

	count
84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

Return:

98

```
: count 10 0 do i 2 +loop ; count
```

step: 2

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:	Return:
10	98

: count 10 0 do i 2 +loop ; count

step: 3

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

0
10

Return:

98

```
: count 10 0 do i 2 +loop ; count
```

step: 4

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

10
0

Return:

98

```
: count 10 0 do i 2 +loop ; count
```

step: 5

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:	Return:
0	10
	98

: count 10 0 do i 2 +loop ; count

step: 6

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

Return:

0
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 7

i

43	RSTK 1
44	RET

Data:

Return:

90
0
10
98

: count 10 0 do i 2 +loop ; count

step: 8

i

43 RSTK 1

44 RET

Data:

0

Return:

90

0

10

98

: count 10 0 do i 2 +loop ; count

step: 9

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

0

Return:

0
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 10

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

2
0

Return:

0
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 11

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

2
0

Return:

92
0
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 12

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

92
2
0

Return:

0
10
98

: count 10 0 do i 2 +loop ; count

step: 13

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

2
92
0

Return:

0
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 14

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

0
2
92
0

Return:

10
98

: count 10 0 do i 2 +loop ; count

step: 15

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

2
92
0

Return:

10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 16

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

92
0

Return:

2
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 17

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

0

Return:

92
2
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 18

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

10
0

Return:

92
2
10
98

: count 10 0 do i 2 +loop ; count

step: 19

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

2
10
0

Return:

92
2
10
98

: count 10 0 do i 2 +loop ; count

step: 20

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

-1
0

Return:

92
2
10
98

: count 10 0 do i 2 +loop ; count

step: 21

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

0
0

Return:

92
2
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 22

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

0
0

Return:

2
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 23

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

0

Return:

2
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 24

i

43	RSTK 1
44	RET

Data:

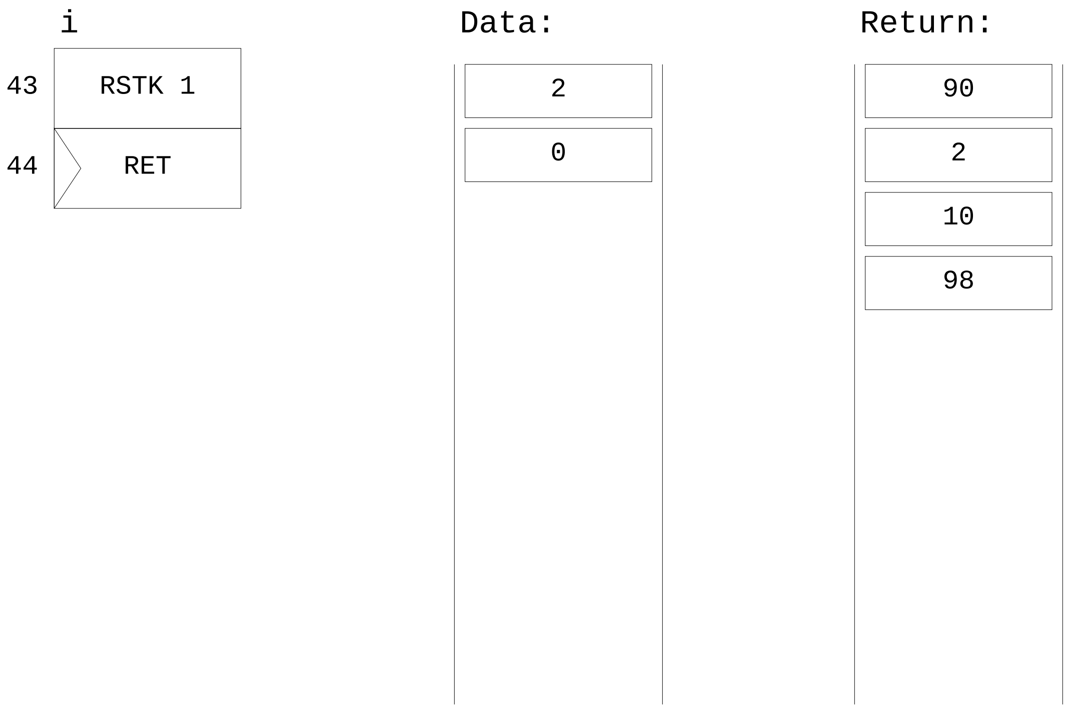
0

Return:

90
2
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 25



```
: count 10 0 do i 2 +loop ; count
```

step: 26

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

2
0

Return:

2
10
98

: count 10 0 do i 2 +loop ; count

step: 27

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

2
2
0

Return:

2
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 28

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

2
2
0

Return:

92
2
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 29

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

92
2
2
0

Return:

2
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 30

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

2
92
2
0

Return:

2
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 31

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

2
2
92
2
0

Return:

10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 32

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

4
92
2
0

Return:

10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 33

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

92
2
0

Return:

4
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 34

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

2
0

Return:

92
4
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 35

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

10
2
0

Return:

92
4
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 36

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

4
10
2
0

Return:

92
4
10
98

: count 10 0 do i 2 +loop ; count

step: 37

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

-1
2
0

Return:

92
4
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 38

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

0
2
0

Return:

92
4
10
98

: count 10 0 do i 2 +loop ; count

step: 39

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

0
2
0

Return:

4
10
98

: count 10 0 do i 2 +loop ; count

step: 40

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

- 2
- 0

Return:

- 4
- 10
- 98

```
: count 10 0 do i 2 +loop ; count
```

step: 41

i

43	RSTK 1
44	RET

Data:

2
0

Return:

90
4
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 42

i

43	RSTK 1
44	RET

Data:

4
2
0

Return:

90
4
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 43

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:	Return:
4	4
2	10
0	98

```
: count 10 0 do i 2 +loop ; count
```

step: 44

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

2
4
2
0

Return:

4
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 45

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

2
4
2
0

Return:

92
4
10
98

: count 10 0 do i 2 +loop ; count

step: 46

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

92
2
4
2
0

Return:

4
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 47

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

2
92
4
2
0

Return:

4
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 48

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

4
2
92
4
2
0

Return:

10
98

: count 10 0 do i 2 +loop ; count

step: 49

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

6
92
4
2
0

Return:

10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 50

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

92
4
2
0

Return:

6
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 51

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

- 4
- 2
- 0

Return:

- 92
- 6
- 10
- 98

```
: count 10 0 do i 2 +loop ; count
```

step: 52

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

10
4
2
0

Return:

92
6
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 53

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:
6
10
4
2
0

Return:
92
6
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 54

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

-1
4
2
0

Return:

92
6
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 55

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:	Return:
0	92
4	6
2	10
0	98

```
: count 10 0 do i 2 +loop ; count
```

step: 56

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

0
4
2
0

Return:

6
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 57

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

4
2
0

Return:

6
10
98

: count 10 0 do i 2 +loop ; count

step: 58

i

43	RSTK 1
44	RET

Data:

4
2
0

Return:

90
6
10
98

: count 10 0 do i 2 +loop ; count

step: 59

i

43	RSTK 1
44	RET

Data:

6
4
2
0

Return:

90
6
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 60

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

6
4
2
0

Return:

6
10
98

: count 10 0 do i 2 +loop ; count

step: 61

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

2
6
4
2
0

Return:

6
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 62

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

2
6
4
2
0

Return:

92
6
10
98

: count 10 0 do i 2 +loop ; count

step: 63

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

92
2
6
4
2
0

Return:

6
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 64

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

2
92
6
4
2
0

Return:

6
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 65

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

6
2
92
6
4
2
0

Return:

10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 66

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

8
92
6
4
2
0

Return:

10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 67

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

92
6
4
2
0

Return:

8
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 68

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

6
4
2
0

Return:

92
8
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 69

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

10
6
4
2
0

Return:

92
8
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 70

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

8
10
6
4
2
0

Return:

92
8
10
98

: count 10 0 do i 2 +loop ; count

step: 71

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

-1
6
4
2
0

Return:

92
8
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 72

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

0
6
4
2
0

Return:

92
8
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 73

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

0
6
4
2
0

Return:

8
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 74

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

6
4
2
0

Return:

8
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 75

i

43	RSTK 1
44	RET

Data:

6
4
2
0

Return:

90
8
10
98

: count 10 0 do i 2 +loop ; count

step: 76

i

43	RSTK 1
44	RET

Data:

8
6
4
2
0

Return:

90
8
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 77

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

8
6
4
2
0

Return:

8
10
98

: count 10 0 do i 2 +loop ; count

step: 78

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

2
8
6
4
2
0

Return:

8
10
98

: count 10 0 do i 2 +loop ; count

step: 79

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

2
8
6
4
2
0

Return:

92
8
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 80

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

92
2
8
6
4
2
0

Return:

8
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 81

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

2
92
8
6
4
2
0

Return:

8
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 82

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

8
2
92
8
6
4
2
0

Return:

10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 83

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

10
92
8
6
4
2
0

Return:

10
98

: count 10 0 do i 2 +loop ; count

step: 84

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

92
8
6
4
2
0

Return:

10
10
98

: count 10 0 do i 2 +loop ; count

step: 85

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

8
6
4
2
0

Return:

92
10
10
98

: count 10 0 do i 2 +loop ; count

step: 86

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

10
8
6
4
2
0

Return:

92
10
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 87

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

10
10
8
6
4
2
0

Return:

92
10
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 88

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

0
8
6
4
2
0

Return:

92
10
10
98

: count 10 0 do i 2 +loop ; count

step: 89

[+loop]

72	RTS
73	SWAP
74	RTS
75	ADD
76	STR
77	STR
78	RSTK 2
79	RSTK 1
80	GTR
81	NOT
82	RET

Data:

-1
8
6
4
2
0

Return:

92
10
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 90

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

-1
8
6
4
2
0

Return:

10
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 91

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

8
6
4
2
0

Return:

10
10
98

```
: count 10 0 do i 2 +loop ; count
```

step: 92

count

84	CONST 10
85	CONST 0
86	SWAP
87	STR
88	STR
89	CALL 43
90	CONST 2
91	CALL 72
92	JUMPZ 89
93	RDROP
94	RDROP
95	RET

Data:

8
6
4
2
0

Return:

10
98

: count 10 0 do i 2 +loop ; count

step: 93

	count	Data:	Return:
84	CONST 10	8	98
85	CONST 0	6	
86	SWAP	4	
87	STR	2	
88	STR	0	
89	CALL 43		
90	CONST 2		
91	CALL 72		
92	JUMPZ 89		
93	RDROP		
94	RDROP		
95	RET		

: count 10 0 do i 2 +loop ; count

step: 94

[entrypoint]

97	CALL 84
98	RET

Data:

8
6
4
2
0

Return: